

What is claimed is:

1. A substrate for a liquid crystal display, comprising:
first bus lines formed in parallel with each other on a base substrate;

second bus lines formed in parallel with each other such that they intersect the first bus lines with an insulation film formed on the first bus lines interposed therebetween;

a first terminal section formed of the same material as that of either of the first and second bus lines, electrically connected to each of the first or second bus lines, and connected to one terminal of a semiconductor chip mounted on a surface of the base substrate;

a second terminal section formed of the same material as that of the other of the first and second bus lines and connected to another terminal of the semiconductor chip;

a third terminal section electrically connected to the second terminal section, and which is provided at an end of the base substrate and to which signals are input from the outside;

a connection wiring electrically connected to the first terminal section; and

a common wiring electrically connected to the first or second bus lines through the connection wiring and provided at an end of the base substrate in a location that is removed during chamfering.

2. A substrate for a liquid crystal display according to claim 1, wherein the common wiring is provided at an end of

the base substrate where the semiconductor chip is mounted.

3. A substrate for a liquid crystal display according to claim 1, further comprising a fourth terminal section connected to each of the first and second bus lines and formed in the region where the semiconductor chip is mounted.

4. A substrate for a liquid crystal display, comprising:
first and second bus lines formed on a base substrate such that they intersect each other with an insulation film interposed therebetween;

a terminal section formed of the same material as that of either of the first and second bus lines, electrically connected to each of the first or second bus lines, and connected to one terminal of a semiconductor chip mounted on a surface of the base substrate; and

another terminal section which is formed in the region where the semiconductor chip is mounted and connected to each of the terminal sections and which is put in contact with a probe pin for inspection.

5. A liquid crystal display comprising:

a first substrate;

a semiconductor chip mounted on a surface of the first substrate;

a second substrate provided opposite to the first substrate and combined with the same; and

a liquid crystal sealed between the first and second

substrates;

wherein the first substrate is a substrate for a liquid crystal display according to claim 1.

6. A method of manufacturing a liquid crystal display, comprising the steps of:

sealing a liquid crystal between a first substrate having a plurality of bus lines formed such that they intersect each other with an insulation film interposed therebetween and a second substrate provided opposite to the first substrate; and

chamfering edges of the first and second substrates by polishing the same and removing a common wiring formed at an end of the first substrate and connected to the plurality of bus lines;

wherein a substrate for a liquid crystal display according to claim 1 is used as the first substrate.

7. A method of manufacturing a liquid crystal display comprising the steps of:

sealing a liquid crystal between a first substrate having a plurality of bus lines formed such that they intersect each other with an insulation film interposed therebetween and a second substrate provided opposite to the first substrate;

chamfering edges of the first and second substrates by polishing the same and removing a common wiring formed at an end of the first substrate and connected to the plurality of bus lines; and

applying conductive paste so as to connect a plurality

of terminal sections each other, the terminal sections being connected to the plurality of bus lines respectively.

8. A method of manufacturing a liquid crystal display according to claim 7, wherein the paste is removed before a semiconductor chip is mounted on a surface of the first substrate.

9. A method of manufacturing a liquid crystal display according to claim 7, wherein a substrate for a liquid crystal display according to claim 1 is used as the first substrate.